



Attorney Docket No. 826.1707

gjc

UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Makoto NAKANISHI

Application No.: 09/811,484

ATTENTION

Confirmation No.: 7934

CERTIFICATE OF CORRECTION

Filed: March 20, 2001

BRANCH

U.S. Patent No.: 6,907,513

Batch No.:

Issued: June 14, 2005

For: MATRIX PROCESSING METHOD OF SHARED-MEMORY SCALAR PARALLEL-  
PROCESSING COMPUTER AND RECORDING MEDIUM

**REQUEST FOR CERTIFICATE OF CORRECTION**

**Certificate**

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**FEB 13 2006**

**of Correction**

Sir:

Patentee(s) respectfully request(s) that a Certificate of Correction be issued in the  
subject patent, pursuant to 35 U.S.C. §254 and 37 C.F.R. §1.322, to correct the mistake(s)  
shown on the attached Form PTO-1050.

Since the mistakes are Patent Office mistakes, it is believed that no fee is required.

Respectfully submitted,

STAAS & HALSEY LLP

Date:

2/6/6

By:

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(Also Form PTO-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO : 6,907,513

DATED : June 14, 2005

INVENTOR(S) : Makoto NAKANISHI

Col. 11, line 61, after "block" delete "carts" and insert --parts--, therefor.

Col. 12, line 12, delete "herein" and insert --wherein--, therefor.

Col. 13, line 18, after "long" insert --in--.

MAILING ADDRESS OF SENDER:

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Washington, DC 20005

PATENT NO. 6,907,513

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